

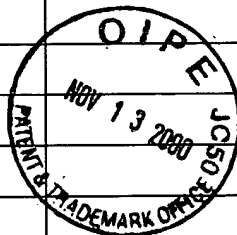
<b>FORM PTO-1449 (Modified)</b> <b>U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)  (37 CFR 1.98(b))	<b>ATTY. DOCKET NO.</b> 1662-29000 (P00-3000)	<b>SERIAL NO.</b> 09/597,190
<b>APPLICANTS</b> William P. BUNTON et al.		<b>GROUP</b> 2731
<b>FILING DATE</b> June 20, 2000		<b>Technology Center 2600</b>

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U. S. PATENT DOCUMENTS												
Examiner Initial	Patent Number							Issue Date	Patentee	Class	Sub-class	Filing Date (If Appropriate)



FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION														
			Document Number						Publication Date	Country or Patent Office	Class	Sub-Class	Translation	
													Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)			
FD	AA	Patent Application Serial No. 09/597,192, filed June 20, 2000, and entitled "High-Speed Interconnection Adapter Having Automated Crossed Differential Pair Correction" by William P. Bunton, John Krause and Patricia L. Whiteside.	
FD	AB	Patent Application Serial No. 09/596,980, filed June 20, 2000, and entitled "High-Speed Interconnection Adapter Having Automated Lane De-Skew" by William P. Bunton, John Krause, Patricia L. Whiteside and Scott Smith.	
FD	AC	"A DC-Balanced, Partitioned-Block, 8B/10B Transmission Code," IBM J. Res. Develop., Vol. 27 No. 5, September 1983 by A. X. Widmer and P.A. Franaszek, pp. 440-451, Tables (6 p.).	
FD	AD	"Compaq's InfiniBand™ Architectural Strategy," Technology Brief, March 2000, pp. 1-11.	
Examiner <i>ml</i>		Date Considered <i>9/22/04</i>	
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			